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Docket No.: M-12366 US

JCESS U.S. PRO  
10/00/1976 12/03/01

December 3, 2001

Box Patent Application  
Commissioner For Patents  
U.S. Patent and Trademark Office  
P.O. Box 2327  
Arlington, VA 22202

Enclosed herewith for filing is a patent application, as follows:

Inventors: Hamid R. Rategh, Mehdi F. Soltan

Title: INTEGRATED CIRCUITS WITH SCALABLE DESIGN

Return Receipt Postcard  
 This Transmittal Letter (in duplicate)  
6 page(s) Specification (not including claims)  
3 pages Claims  
1 page Abstract  
3 Sheets of Drawings  
2 pages Declaration For Patent Application and Power of Attorney  
1 page Recordation Form Cover Sheet (in duplicate)  
2 pages Assignment

Applicants assert entitlement to small entity status for the attached patent application

**CLAIMS AS FILED (fees computed under 37 CFR §1.9(f))**

For	Number Filed	Number Extra	Rate	\$	Basic Fee
Total Claims	8	-20	= 0	x \$ 9.00	= \$ 370.00
Independent Claims	3	-3	= 0	x \$42.00	= \$ 0.00
<input type="checkbox"/> Fee of _____ for the first filing of one or more multiple dependent claims per application				\$	
<input type="checkbox"/> Fee for Request for Extension of Time				\$	

Please make the following charges to Deposit Account 19-2386:

Total fee for filing the patent application in the amount of \$ 370.00  
 The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 19-2386.

EXPRESS MAIL LABEL NO:  
EL 873 332 337 US

Respectfully submitted,

  
N. R. H. Black

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